

CLEAN VERSION WITH CHANGES INCORPORATED

IN THE SPECIFICATION

- o The following new paragraph has been inserted after page 3, line 31:

—Fig. 5 shows a flow diagram of the processing of each checkbit generator in Fig. 3, according to one embodiment of the present invention.—

- o The following new paragraph has been inserted after page 7, line 16:

—Fig. 5 shows a flow diagram of the processing of each checkbit generator 306 in Fig. 3, according to one embodiment of the present invention. In particular, checkbit generator 306 divides the corresponding sub-signal into one or more subsets of data (step 502 of Fig. 5), applies a checksum function to each subset of data to generate a checkbit for the subset (step 504), adds the checkbit for each subset to the sub-signal to generate an augmented sub-signal (step 506), and transmits at least two copies of the augmented sub-signal (step 508) for routing in parallel through redundant portions of a distributed switch fabric to generate at least two routed sub-signals for the sub-signal.—